

In the Claims:

**Please cancel claims 1-7, 9-12 and 14-42 without prejudice such that the claims will read as follows:**

Claims 1-7 (canceled).

Claim 8 (previously presented): A method of driving a clock tree on an integrated circuit (IC), the method comprising the steps of:

providing an IC having a clock tree;

distributing a clock signal in the form of a differential sinusoidal signal pair in a portion of the clock tree, the differential sinusoidal signal pair comprising a first sinusoidal signal and a second sinusoidal signal; and

generating a local clock signal from the differential pair by employing both the first sinusoidal signal and the second sinusoidal signal to form the local clock signal.

Claims 9-12 (canceled).

Claim 13 (previously presented): A clock circuit for an IC, comprising:

a generating circuit adapted to generate a differential sinusoidal signal pair comprising a first sinusoidal signal and a second sinusoidal signal;

a distribution circuit coupled to the generating circuit and adapted to distribute the differential sinusoidal signal pair on the IC; and

a plurality of clock receiver circuits coupled to the distribution circuit and adapted to convert the differential

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sinusoidal signal pair into respective local clock signals by employing both the first sinusoidal signal and the second sinusoidal signal to form each local clock signal.

Claims 14-42 (canceled).